

# **New Miran III that never completes boot process because nothing was configured in the PBX !**

By BaBosch - Jan 14, 2010

## **Initial boot hangs here -See these messages over & over**

```
0xf3c34c (tStart): Card Lan message and no Companding Law..  
0xf3c34c (tStart): Wait for M-1 Companding Law no timeout...  
0xf3c34c (tStart): Card Lan message and no Companding Law..  
0xf3c34c (tStart): Card Lan message and no Companding Law..
```

## **Pack finally reboots by itself**

```
Rebooting Pack ...  
0xf3c34c (tStart): Wait for M-1 Companding Law no timeout...  
0xf3c34c (tStart): Card Lan message and no Companding Law..
```

```
MC Firmware Rls 8.5  
8051XA Firmware Version 4.11 05 November 2003  
(C) Nortel Inc. 2004  
EPLD Version: 1.1  
64K External RAM detected  
8K DPRAM detected  
Dongle serial number: 10391090  
All FPGAs are configured  
All self tests have passed  
SRAM test okay  
SDRAM Addr test okay  
SDRAM blank over  
Early serial debug initialized
```

VxWorks System Boot

Copyright 1984-1998 Wind River Systems, Inc.

```
CPU: IXP1200  
Version: 5.4  
BSP version: 5.2  
Creation date: Feb 24 2003, 11:03:32
```

Enter jkl to force boot to BootROM vxWorks prompt

auto-booting...

```
boot device      : ata=
unit number     : 1
processor number : 0
file name       : /C:/exec
flags (f)       : 0x0
other (o)       : ixpMac
```

```
Attaching to ATA disk device... Found device : Cirrus CL-PD672x
PC Card Detected: Socket 1
cisGet : pcCard recognized
/C:/ - Volume is OK
2644068 + 182444 + 798716
Starting at 0x1000...
```

```
Early serial debug initialized
Setting tLogTask priority to 250
Found device : Cirrus CL-PD672x
0x1affe64 (tRootTask): PC Card Detected: Socket 1
logTask: 2 log messages lost.
ipMethodInit, gIPMethod = 1
lnIsaNvramInfoGet: ipMethod = 1, Boot Line not updated
Host Name: bootHost
Target Name: vxTarget
User: target
```

```
MAC Address for Port 0 is : 00:1b:ba:f5:5d:c2
SysConf: uEng_intConnect to pktHandlingIsr.
0 symbols bound.
Load uCode from buffer Status : Return Success
Starting Microengines.
0x1affe64 (tRootTask): Loading MAC_21440...
0x1affe64 (tRootTask): mac21440Load: Init String[0] NULL: ixpMac
0x1affe64 (tRootTask): Loading MAC_21440...
0x1affe64 (tRootTask): MAC 21440 String : Processed all arugments
0x1affe64 (tRootTask): mac21440InitMem: Net Pool ID is 18313148
0x1affe64 (tRootTask): Done loading mac21440...
0x1affe64 (tRootTask): mac21440Start: Starting port 0
TLAN Port set to Auto-Negotiate Speed and Duplex Settings
ELAN set to 10BaseT Operation
ELAN set to Half Duplex Operation
Waiting For Carrier link to Return
```

```
MAC Address for Port 1 is : 00:1b:ba:f5:5d:c3
0x1affe64 (tRootTask): Carrier still down after 10 Seconds
0x1affe64 (tRootTask): Loading MAC_21440...
0x1affe64 (tRootTask): mac21440Load: Init String[0] NULL: ixpMac
0x1affe64 (tRootTask): Loading MAC_21440...
0x1affe64 (tRootTask): MAC 21440 String : Processed all arugments
0x1affe64 (tRootTask): mac21440InitMem: Net Pool ID is 16460576
0x1affe64 (tRootTask): Done loading mac21440...
0x1affe64 (tRootTask): mac21440Start: Starting port 1
TLAN Port set to Auto-Negotiate Speed and Duplex Settings
ELAN set to 10BaseT Operation
ELAN set to Half Duplex Operation
Waiting For Carrier link to Return
0x1affe64 (tRootTask): Carrier still down after 10 Seconds
Getting IP over BOOTP via network interface ixpMac1
Enter +++ to escape to Shell for manual configuration
.++
```

**Entered "+++" to bypass bootP process (always for new install)**

escape sequence detected - configure network manually...

[illegible]

```
CPU: IXP1200.  Processor #0.  
Memory Size: 0x1b00000.  BSP version 3.2+Miran.
```

```
/C:/ - Volume is OK
->
```

```

Welcome to Miran 3...
0xf3c34c (tStart): Init M-1 Manager
0xf3c34c (tStart): Init XA
0xf3c34c (tStart): XA : - Create MsgQ (Struct)
0xf3c34c (tStart): XA : - Register with M-1 Manager
0xf3c34c (tStart): XA : - Spawn xaTask()
0xf344a0 (tXA): Task Spawned OK
0xf3c34c (tStart): NEW a07ManagerInit : Send App Active to XA
0xf3c34c (tStart): XA : - Query 8051XA for Dongle ID
0xf3c34c (tStart): oamInit : Initialise OA&M...
0xf3c34c (tStart): strTableInit : 590 Strings
0xf3c34c (tStart): Initialise Commands
0xf3c34c (tStart): Initialising Menus
0xf3c34c (tStart): Menus OK
0xf3c34c (tStart): Initialise Configuration Variables
0xf3c34c (tStart): Loading Users...
0xf3c34c (tStart): - User File not found...
0xf3c34c (tStart): - User Backup File not found
0xf3c34c (tStart): a07ManagerInit : Create MsgQ (Struct)
0xf3c34c (tStart): a07ManagerInit : A07 MsgQ created 0xf2ea74
0xf3c34c (tStart): a07ManagerInit : Registered with M-1 Manager
0xf3c34c (tStart): a07ManagerInit : Task spawned
0xf3c34c (tStart): a07ManagerInit : Send App Active to XA
0xf344a0 (tXA): Got Dongle Response
0xf344a0 (tXA): Dongle : 10391090
0xf344a0 (tXA): Got Version Response
0xf344a0 (tXA): Version : MC Firmware Rls 8.5
0xf344a0 (tXA): Version > 7.0, Get Serial Number
0xf344a0 (tXA): Got Serial Number Response
0xf344a0 (tXA): Serial :
0xf3c34c (tStart): Wait for Firmware Version (card Lan) on timeout..

```

Pack is now stuck again...waiting for Firmware Version as noted above

```
0xf3c34c (tStart): Check for M-1 Companding Law...
0xf3c34c (tStart): Wait for M-1 Companding Law no timeout...
```

```

0xf3c34c (tStart): Card Lan message and no Companding Law..
0xf3c34c (tStart): Wait for M-1 Companding Law no timeout...
0xf3c34c (tStart): Card Lan message and no Companding Law..
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0xf3c34c (tStart): Card Lan message and no Companding Law..

```

**Finally realized you have to add at least a DID trunk in the PBX on the Miran Slot and enabled the card.**

Now you get the same boot process as above without the hang.

The boot process then continues as shown immediately after the `0xf344a0 (tXA): Serial :` line above. ^

**No more>>> 0xf3c34c (tStart): Check for M-1 Companding Law...**

**0xf3c34c (tStart): Wait for M-1 Companding Law no timeout...errors**

```

0xf2e8c8 (tA07): (00) 0x30 0x00 Board Download <<<<This is what was never happening!
0xf2e8c8 (tA07): - Companding Law = Mu-Law
0xf2e8c8 (tA07): - BoardParam->10PPS1 Make/Break Ratio = 0x00
0xf2e8c8 (tA07): (00) 0x31 0x00 Board Download
0xf2e8c8 (tA07): - BoardParam->10PPS2 Make/Break Ratio = 0x00
0xf2e8c8 (tA07): (00) 0x32 0x00 Board Download
0xf2e8c8 (tA07): - BoardParam->20PPS Make/Break Ratio = 0x00
0xf2e8c8 (tA07): Units enabled : 0x1
0xf2e8c8 (tA07): - Board enabled - LED OFF
0xf3c34c (tStart): Wait for Firmware Version (card Lan) on timeout...
0xf3c34c (tStart): Check for M-1 Companding Law...
0xf3c34c (tStart): dspmInit : Initialising DSPM
0xf0d38c (tDSPM): tDSPM suspended
0xf3c34c (tStart): Init DSP Utilities
0xf3c34c (tStart): tDspUtils : Init DSP Utils...
0xf3c34c (tStart): tDspUtils : Create MsgQ (Struct)
0xf3c34c (tStart): tDspUtils : Spawn dspuTask()
0xf3c34c (tStart): tDspUtils : Task spawned
0xf3c34c (tStart): Codec Driver Version = MC1.00
0xf3c34c (tStart): Found CS4281 PCI Audio Accelerator Device.
0xf3c34c (tStart): - Initialising CS4281 Base Registers :
0xf3c34c (tStart): - PCI Codec Base Address Initialised.
0xf3c34c (tStart): - Unlocking CS4281 CWPR Register :
0xf3c34c (tStart): - Blast Clock Control Register
0xf3c34c (tStart): - Drive ARST High to Low to High
0xf3c34c (tStart): - Turn on the Sound System clocks
0xf3c34c (tStart): - Turn on Power
0xf3c34c (tStart): - Wait for Clock Stabilise
0xf3c34c (tStart): - Enable ASYNC generation
0xf3c34c (tStart): - Wait for the codec ready signal from the AC97 codec
0xf3c34c (tStart): - Assert the 'valid frame' signal so we can talk to the AC97 codec
0xf3c34c (tStart): - Wait until we've sampled input slots 3 & 4 as valid
0xf3c34c (tStart): Codec initialisation complete - AC97 Access now possible.
0xf3c34c (tStart): Found Codec Device :
0xf3c34c (tStart): CS4299 Revision 4.

```

```

0xf3c34c (tStart): - CODEC ADC Power Up.....
0xf3c34c (tStart): - ADC Power OK.
0xf3c34c (tStart): - Capture DMA FIFO Set-Up Complete.
0xf3c34c (tStart): - Interrupts Enabled for Capture.
0xf3c34c (tStart): - DMA Control Register Configured.
0xf3c34c (tStart): - DMA1 Interrupt Enabled
0xf3c34c (tStart): - Initialised IXP1200 PCI DRAM Pointers.
0xf3c34c (tStart): - Interrupt 3 enabled for Codec
0xf3c34c (tStart): - Codec Int Handler connected
0xefaa78 (tBui): httpd : Setup HTTP Aliasing
0xefaa78 (tBui): httpd : Setup IP Checking
0xefaa78 (tBui): httpd : Setup Password Checking
0xefaa78 (tBui): httpd : Setup Access Control
0xefaa78 (tBui): httpd : Setup HTTP File System
0xefaa78 (tBui): httpd : Setup Server Side Includes
0xefaa78 (tBui): httpd : dynaListInit
0xefaa78 (tBui): httpd : buiSsiInit
0xefaa78 (tBui): httpd : buiNavTreeInit
0xefaa78 (tBui): httpd : buiVarInit
0xefaa78 (tBui): httpd : buiRpmInit
0xefaa78 (tBui): Initialise RPM Table
0xefaa78 (tBui): 14 RPM entries configured
0xefaa78 (tBui): httpd : buiPageInfoInit
0xefaa78 (tBui): httpd : Setup Memory File System
0xefaa78 (tBui): httpd: Start Web Server...
0xefaa78 (tBui): httpd: httpServerLoop() failed (result = -1)
0xf3c34c (tStart): Timeout waiting for BUI.
0xf3c34c (tStart): TUI Prompts Uncompressed OK
0xf3c34c (tStart): dspmInit : Initialising DSP Loadware...
0xf3c34c (tStart): dspmStart : Downloading DSP Loadware (Mem)...
0xf3c34c (tStart): dmaInit : Initialising DMA...
0xf3c34c (tStart): Resetting Playthru
0xf0d38c (tDSPM): tDSPM resuming...
0xf051e0 (tDspUtils): Dsp Core 0 : Booted OK
0xf3c34c (tStart): Setting Record Gain to 0x808
0xf3c34c (tStart): Setting Record to 20 bit
0xf3c34c (tStart): dWCount Initialised Value : 0x00000400
0xf3c34c (tStart): Setting Codec DMA Base Address Value to : 0xc0004000
0xf3c34c (tStart): Value for DMA Base Address confirmed as : 0xc0004000
0xf3c34c (tStart): Setting Codec DMA Count Value to : 0x000003ff
0xf3c34c (tStart): Value for DMA Count Value confirmed as : 0x000003ff
0xf3c34c (tStart): Enabling interrupts - Write 0x3 to 0x62000008
0xf3c34c (tStart): Value from 0x62000008 confirmed as 0x1
0xf3c34c (tStart): Setting Codec DMA Channel Mask to : 0x00010000
0xf3c34c (tStart): Write 0x10000 to 0x6200015c
0xf3c34c (tStart): Value for DMA Channel Mask confirmed as : 0x00010000
0xf3c34c (tStart): Creating 2048K RAM Block Device
0xf3c34c (tStart): pBlkDevRam = 0xdf64d8
0xf3c34c
No access to : 0 (tStart): 2048K RAM Drive created (fp = 0xdf62e4)
0xf3c34c (tStart): Configuration Variables file not found, using defaults
0xf3c34c (tStart): Language File not found on /C:
0xf3c34c (tStart): Retrieving Dongle ID from XA...
0xf3c34c (tStart): Dongle ID received from XA after 0 s: 10391090
0xf3c34c (tStart): Keycode file not found
0xf3c34c (tStart): Init Calendar
0xf3c34c (tStart): - Loading Descriptor File
0xf3c34c (tStart): - Descriptor File not found
0xf3c34c (tStart): - Adding default Descriptors
0xf3c34c (tStart): - Loading Calendar File
0xf3c34c (tStart): - Calendar File not found
0xf3c34c (tStart): - Calendar size = 0, pUser = 0x371d88
0xf3c34c (tStart): - Auto assigning Music from /C:

```

```
0xf3c34c (tStart): Init RAN Application...
0xf3c34c (tStart): Codec : Registered App 'RAN App'
0xf3c34c (tStart): Init TUI Application
0xf3c34c (tStart): Init A07 Manager
0xf3c34c (tStart): a07ManagerInit : Initialise A07 Manager...
0xf3c34c (tStart): a07ManagerInit : Send Card LAN Reset to XA
0xf3c34c (tStart): Wait for M-1 Companding Law 2...
0xf3c34c (tStart): M-1 Companding Law is Mu-Law
0xf3c34c (tStart): xaToHostPollingEnable: XA to Host Polling Enabled.
0xf3c34c (tStart): miranMaintEnable : Maintenance Mode enabled
0xf3c34c (tStart): Initialising VT100 Interface...
0xf2e8c8 (tA07): (00) 0x30 0x00 Board Download
0xf2e8c8 (tA07): - BoardParam->10PPS1 Make/Break Ratio = 0x00
0xf2e8c8 (tA07): tDspUtils : dspuGotLaw(0) Mu-Law
0xf2e8c8 (tA07): tDspUtils : Waiting for DSP Core to boot...
0xf2e8c8 (tA07): tDspUtils : DSP Core booted
0xf2e8c8 (tA07): tDspUtils : Setting Core law...
0xf2e8c8 (tA07): (00) 0x31 0x00 Board Download
0xf2e8c8 (tA07): - BoardParam->10PPS2 Make/Break Ratio = 0x00
0xf2e8c8 (tA0
```

```
[10391090] - Log On -
```

```
[Miran3] #####  #####  ## @@@@@@ @@@ @@@ @ @ ]]] [ ] [[[[
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```

```
Meridian Integrated RAN Application
Version 3.1 (Rls 1)
Copyright (c) Nortel Networks. 2001
```

```
Username : -> <-
Password :
```

```
- Log On -
```

```
- Status -
```